

Abstract

To provide a leak current compensating device which ensures that the voltage of the output terminal is made to ground potential while minimizing sink current flowing from the output terminal, when the output transistor goes into the OFF state. The leak current compensating device according to the present invention comprises a first power source terminal; a second power source terminal having a lower potential than the first power source terminal; an output terminal; a first transistor which is connected at one end to the first power source terminal and which has a conductive state, in which the first transistor outputs a predetermined voltage or current from the other end to the output terminal, and a cut-off state; a second transistor of the same kind as the first transistor which is connected at one end to the first power source terminal and set in the cut-off state; a third transistor which is interposed at a path for the flow of a leak current output from the other end of the second transistor to the second power source terminal, and a control terminal of which is connected to the path; and a fourth transistor which constitutes a current mirror circuit with the third transistor and has a drive

capacity to pass a current corresponding to the current flowing through the third transistor from the output terminal to the second power source terminal.